

Electronic Acknowledgement Receipt

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Application Number:	10501651
International Application Number:	
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Title of Invention:	ON-CHIP ESD PROTECTION CIRCUIT FOR COMPOUND SEMICONDUCTOR HETEROJUNCTION BIPOLAR TRANSISTOR RF CIRCUITS
First Named Inventor/Applicant Name:	Yin Tat Ma
Customer Number:	23386
Filer:	Feng Ma
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Refund Request	Request_Refund.pdf	138392 ad8d12b5b159e61e0ffada7b1fe4eb650 0cfe660	no	2

Warnings:

Information:					
2	Miscellaneous Incoming Letter	Extension_Time.pdf	138398	no	2
			a74c27dc4cab4c8211cbfa5edcd001e270fb070e		
Warnings:					
Information:					
Total Files Size (in bytes):			276790		
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